

RIZ MUGHAL

QUIZ MASTER

Grand Quiz(CS609)

100% correct solution.

For more information you can visit my channel and for any type of help related to CS619 you can contact me.



YOUTUBE CHANNEL:

<https://www.youtube.com/channel/UCINsFwDiB62SValCcPDZbRQ/playlists>

FACEBOOK GROUP:

<https://www.facebook.com/groups/923887914750307>

CS609:Grand Quiz

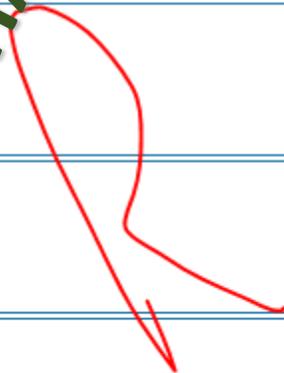
Question # 1 of 30 (Start time: 02:04:45 AM, 26 December 2020)

_____ stores the base address for LPT1

Select the correct option

- | | |
|----------------------------------|---------|
| <input type="radio"/> | 40:00 H |
| <input type="radio"/> | 40:04 H |
| <input checked="" type="radio"/> | 40:08 H |
| <input type="radio"/> | 40:1A H |

RIZ MUGHAL



CS609:Grand Quiz

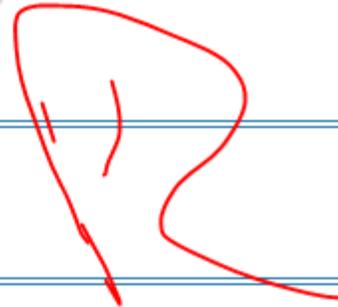
Question # 2 of 30 (Start time: 02:05:13 AM, 26 December 2020)

In interrupt 17H, if DX register contains zero, then I/O will be performed on _____.

Select the correct option

- | | |
|----------------------------------|------|
| <input checked="" type="radio"/> | LPT1 |
| <input type="radio"/> | LPT0 |
| <input type="radio"/> | LPT3 |
| <input type="radio"/> | LPT4 |

RIZ MUGHAL



CS609:Grand Quiz

Question # 3 of 30 (Start time: 02:05:33 AM, 26 December 2020)

The speed of Microprocessor depends upon _____.

Select the correct option

<input type="radio"/>	Memory
<input checked="" type="radio"/>	Frequency of Clock signal
<input type="radio"/>	BIOS
<input type="radio"/>	Operating system

RIZ MUGHAL

CS609:Grand Quiz

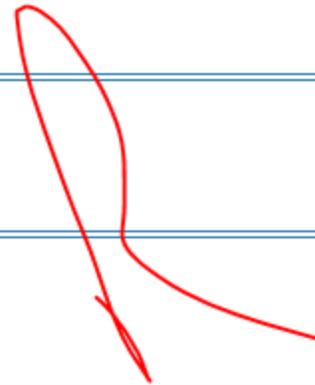
Question # 4 of 30 (Start time: 02:05:47 AM, 26 December 20

Interrupt _____ is empty; we can use its vector as a flag.

Select the correct option

- | | |
|----------------------------------|-----|
| <input type="radio"/> | 9H |
| <input type="radio"/> | 13H |
| <input type="radio"/> | 15H |
| <input checked="" type="radio"/> | 65H |

RIZ MUGHAL



CS609:Grand Quiz

Question # 5 of 30 (Start time: 02:06:03 AM 26 December 2020)

The Interrupt no. 65H is used _____.

Select the correct option

- | | |
|----------------------------------|----------------------------|
| <input type="radio"/> | by DOS |
| <input type="radio"/> | by BIOS |
| <input checked="" type="radio"/> | neither by DOS nor by BIOS |
| <input type="radio"/> | both by DOS and BIOS |

RIZ MUGHAL

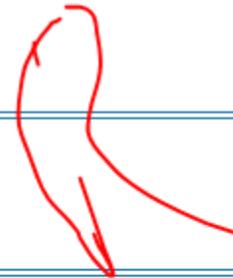
Question # 6 of 30 (Start time: 02:06:18 AM, 26 December 2020)

Timer interrupt is invoked by means of _____.

Select the correct option

- | | |
|----------------------------------|-----------------------|
| <input checked="" type="radio"/> | Hardware |
| <input type="radio"/> | Software |
| <input type="radio"/> | Hardware and Software |
| <input type="radio"/> | Operating System Call |

RIZ MUGHAL



Question # 7 of 30 (Start time: 02:06:37 AM, 26 December 2020)

In Real Time Clock, the Status _____ is used to specify the date time formats.

Select the correct option

<input checked="" type="radio"/>	Register B
<input type="radio"/>	Register C
<input type="radio"/>	Register D
<input type="radio"/>	Register E

RIZ MUGHAL

CS609:Grand Quiz

Question # 8 of 30 (Start time: 02:06:49 AM, 26 December 2020)

The UART device uses _____ for error detection.

Select the correct option

- | | |
|----------------------------------|------------|
| <input type="radio"/> | Checksum |
| <input type="radio"/> | CRC Check |
| <input checked="" type="radio"/> | Parity Bit |
| <input type="radio"/> | LRC Check |

RIZ MUGHAL

CS609:Grand Quiz

Question # 9 of 30 (Start time: 02:07:02 AM, 26 December 2020)

BIOS data area starts from _____.

Select the correct option

- | | |
|----------------------------------|--------|
| <input checked="" type="radio"/> | 40:00H |
| <input type="radio"/> | 40:1EH |
| <input type="radio"/> | 40:3DH |
| <input type="radio"/> | 40:FFH |

RIZ MUGHAL

Question # 10 of 30 (Start time: 02:07:15 AM, 26 December 2020)

In case of synchronous communication a timing signal is required to identify the start and end of a bit.

Select the correct option

<input checked="" type="radio"/>	TRUE
<input type="radio"/>	FALSE

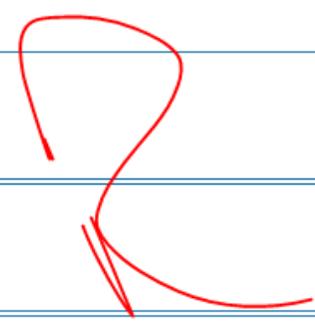
RIZ MUGHAL

Question # 11 of 30 (Start time: 02:07:31 AM, 26 December 2020)

Total Marks

The registers Flags, _____ are pushed on stack after execution of INT instruction and execution branches to the interrupt procedure.

Select the correct option

- | | |
|----------------------------------|-----------|
| <input checked="" type="radio"/> | CS and IP |
| <input type="radio"/> | CS and SP |
| <input type="radio"/> | IP and DS |
| <input type="radio"/> | DS and SP |
- RIZ MUGHAL**
- 

CS609: Grand Quiz

Question # 12 of 30 (Start time: 02:07:47 AM, 26 December 2020)

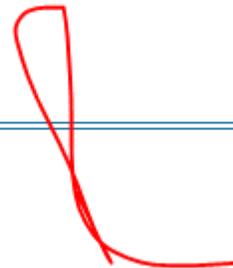
What is output of the following program;

```
#include<DOS.H>
union REGS regs;
void main (void )
{
regs.h.al = 0x99;
```

Select the correct option

<input type="radio"/>	00
<input checked="" type="radio"/>	99
<input type="radio"/>	0099
<input type="radio"/>	9900

RIZ MUGHAL



CS609:Grand Quiz

Question # 13 of 30 (Start time: 02:08:23 AM, 26 December 2020)

Parameters in interrupts are passed through _____ .

Select the correct option

- | | |
|----------------------------------|------------|
| <input type="radio"/> | stack |
| <input checked="" type="radio"/> | registers |
| <input type="radio"/> | array |
| <input type="radio"/> | link lists |

RIZ MUGHAL

CS609:Grand Quiz

Question # 14 of 30 (Start time: 02:08:41 AM, 26 December 2021)

The printer interface uses the _____.

Select the correct option

- | | |
|----------------------------------|-------|
| <input type="radio"/> | IRQ 6 |
| <input checked="" type="radio"/> | IRQ 7 |
| <input type="radio"/> | IRQ 1 |
| <input type="radio"/> | IRQ 2 |

RIZ MUGHAL

Question # 15 of 30 (Start time: 02:08:55 AM, 26 December 2020)

Total Marks: 1

While performing programmed I/O on the printer using BIOS service, we must check _____ of Status Byte to see if printer is busy or not.

Select the correct option

- | | | |
|----------------------------------|-----------------------|----|
| <input checked="" type="radio"/> | Most Significant Bit | // |
| <input type="radio"/> | Least Significant Bit | // |
| <input type="radio"/> | Fourth Bit | // |
| <input type="radio"/> | Tenth Bit | // |

Question # 16 of 30 (Start time: 02:09:10 AM, 26 December 2020)

If printer is _____ then printer sends back the ACK signal to the printer interface

Select the correct option

- idle
- busy
- Out-of-paper
- in paper jam error

RIZ MUGHAL

CS609:Grand Quiz

Question # 17 of 30 (Start time: 02:09:22 AM, 26 December 2020)

REGS is a _____.

Select the correct option

- | | |
|----------------------------------|-----------|
| <input checked="" type="radio"/> | Union |
| <input type="radio"/> | Struct |
| <input type="radio"/> | Interrupt |
| <input type="radio"/> | Service |

RIZ MUGHAL

Question # 18 of 30 (Start time: 02:09:34 AM, 26 December 2020)

Total I

Every ISR (Interrupt Service Routine) should check service number in _____ register and then perform the function accordingly.

Select the correct option

<input checked="" type="radio"/>	AH
<input type="radio"/>	AL
<input type="radio"/>	CH
<input type="radio"/>	CL

RIZ MUGHAL

R

CS609:Grand Quiz

Question # 19 of 30 (Start time: 02:09:49 AM, 26 December 2020)

The register which is not available in the UART device is _____

Select the correct option

- | | |
|----------------------------------|--------------------------|
| <input type="radio"/> | Receiver Buffer Register |
| <input checked="" type="radio"/> | Interrupt Focus Register |
| <input type="radio"/> | Line Status Register |
| <input type="radio"/> | Interrupt ID Register |

RIZ MUGHAL

CS609:Grand Quiz

Question # 20 of 30 (Start time: 02:10:02 AM, 26 December 2020)

DCE is _____

Select the correct option

- | | |
|----------------------------------|------------------------------|
| <input checked="" type="radio"/> | Data Communication Equipment |
| <input type="radio"/> | Dual Communication Equipment |
| <input type="radio"/> | Data Clearing Equipment |
| <input type="radio"/> | Data Control Equipment |

RIZ MUGHAL

Question # 21 of 30 (Start time: 02:10:14 AM, 26 December 2020)

In the Counter Register of Interval Timer, bit number 3 changes its value between 0 and 1 within ____ clock cycles

Select the correct option

- 1
- 2
- 4
- 16

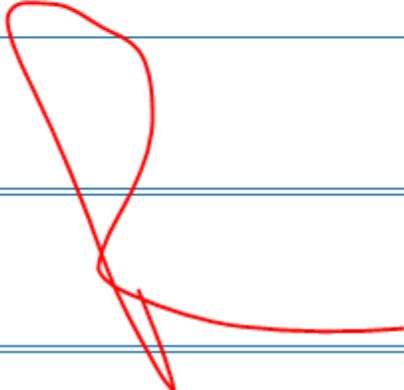
RIZ MUGHAL



Question # 22 of 30 (Start time: 02:10:29 AM, 26 December 2020)

Operating system information is in a special block on disk which is called _____.

Select the correct option

- | | |
|----------------------------------|-----------------|
| <input type="radio"/> | Interrupt Block |
| <input type="radio"/> | OS Block |
| <input type="radio"/> | Secondary Block |
| <input checked="" type="radio"/> | Boot Block |
- RIZ MUGHAL*
- 

CS609:Grand Quiz

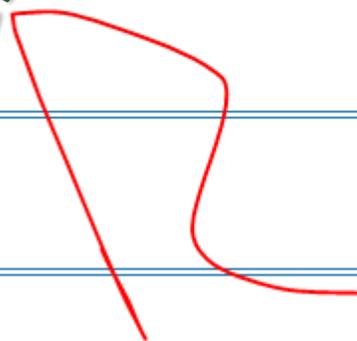
Question # 23 of 30 (Start time: 02:10:44 AM, 26 December 2020)

The PPI acts as an interface between the CPU and a parallel _____ .

Select the correct option

- | | |
|----------------------------------|-------------|
| <input checked="" type="radio"/> | I/O device |
| <input type="radio"/> | Controller |
| <input type="radio"/> | Control bus |
| <input type="radio"/> | Data bus |

RIZ MUGHAL



CS609:Grand Quiz

Question # 24 of 30 (Start time: 02:10:57 AM, 26 December 2020)

All Interval Timer channels are _____ bits wide.

Select the correct option

<input type="radio"/>	4
<input type="radio"/>	8
<input checked="" type="radio"/>	16
<input type="radio"/>	32

RIZ MUGHAL

Question # 25 of 30 (Start time: 02:11:13 AM, 26 December 2020)

In interrupt interception with software interrupts, the original interrupt can be invoked _____

Select the correct option

- | | |
|----------------------------------|----------------------|
| <input type="radio"/> | At the start of ISR |
| <input type="radio"/> | At the end of ISR |
| <input type="radio"/> | In the middle of ISR |
| <input checked="" type="radio"/> | Anywhere in ISR |

CS609:Grand Quiz

Question # 26 of 30 (Start time: 02:11:30 AM, 26 December 2020)

_____ is used to set date.

Select the correct option

- | | |
|----------------------------------|--------|
| <input type="radio"/> | 1A/02H |
| <input type="radio"/> | 1A/03H |
| <input type="radio"/> | 1A/04H |
| <input checked="" type="radio"/> | 1A/05H |

RIZ MUGHAL

CS609:Grand Quiz

Question # 27 of 30 (Start time: 02:11:43 AM, 26 December 2020)

Frequency of clock signal measures the speed of the _____

Select the correct option

- | | |
|----------------------------------|-------------|
| <input type="radio"/> | RAM |
| <input type="radio"/> | BIOS |
| <input checked="" type="radio"/> | CPU |
| <input type="radio"/> | Address Bus |

RIZ MUGHAL

CS609:Grand Quiz

Question # 28 of 30 (Start time: 02:11:57 AM, 26 December 2020)

To store each character in keyboard buffer ____ bytes are required.

Select the correct option

- | | |
|----------------------------------|---|
| <input checked="" type="radio"/> | 2 |
| <input type="radio"/> | 4 |
| <input type="radio"/> | 6 |
| <input type="radio"/> | 8 |

RIZ MUGHAL

CS609:Grand Quiz

Question # 29 of 30 (Start time: 02:12:11 AM, 26 December 2020)

In the context of COM ports, AH register is treated as a _____.

Select the correct option

<input checked="" type="radio"/>	Line Status Register
<input type="radio"/>	Modem Status Register
<input type="radio"/>	Control Register
<input type="radio"/>	Command Register

RIZ MUGHAL

CS609:Grand Quiz

Question # 30 of 30 (Start time: 02:12:27 AM, 26 December 2020)

How many Interrupts can be addressed via Interrupt Vector Table?

Select the correct option

- | | |
|----------------------------------|------|
| <input type="radio"/> | 4 |
| <input checked="" type="radio"/> | 256 |
| <input type="radio"/> | 512 |
| <input type="radio"/> | 1024 |

RIZ MUGHAL

