Cs501 Quiz

Solved by Rizwan Qadeer (Riz Mughal)
BSCS Completed
SQA Engineer
I can solve any quiz of any book from any Department
With 100% result
Guarantee marks

Youtube link:

https://www.youtube.com/channel/UCINsFwDiB62SValCcPDZbRQ/playlists

Question # 1 of 10 (Start time: 12:06:10 PM, 09 August 2020)   **Total Marks:** 1

The information about interrupt vector is given in 8-bits, from bit 0 to 7, which is translated to bit _____ on the data bus.

Select the correct option

- ⦿ 16 to 23
- ○ 11 to 18
- ○ 0 to 7
- ○ 8 to 15

CS501:Semester Quiz # 01

_____ is the simplest form for representing a signed number

Select the correct option

○	Biased Representation
○	Diminished Radix Compliment Form
◉	Sign Magnitude Form
○	None of the given

Question # 3 of 10 (Start time: 12:06:58 PM, 09 August 2020)

_____ signal is used in printer with DB-25 interface to reset its controller.

Select the correct option

○ #PE

○ #STROB

◉ #INIT

○ #SLCT

Question # 4 of 10 (Start time: 12:07:16 PM, 09 August 2020)   **Tota**

A component connected to the system bus and having control of it during a particular bus cycle is called_____.

Select the correct option

○ Slave component

◉ Master component

○ System bus

○ Buffer component

Question # 5 of 10 (Start time: 12:07:36 PM, 09 August 2020)

Most parallel I/O ports used with peripheral devices are mapped on a range of_____.

Select the correct option

○	Bus addresses
○	Direct memory Access
◉	contiguous addresses
○	Cache

CS501:Semester Quiz # 01

_____ is/are example(s) of synchronous communication.

Select the correct option

⬤	Register to Register
◯	Register to Memory
◯	Memory to Memory
◯	All of the given

Taking control of the system bus for a few bus cycles is known as _____.

Select the correct option

O	Bus Stealing
◉	Cycle Stealing
O	Cycle Transfering
O	None of given

Question # 8 of 10 (Start time: 12:08:20 PM, 09 August 2020)

In a printer with DB-25 interface, _____ signal is is better for edge triggered systems.

Select the correct option

○ BUSY#

○ PE#

● ACKNLG#

○ STROB#

Question # 9 of 10 (Start time: 12:08:37 PM, 09 August 2020)

For input ports, the incoming data should be placed on
the data bus only during the I/O read bus cycle. For this purpose, _____ are used.

Select the correct option

○	Flip Flops
●	Tri-state Buffers
○	AND Gates
○	Registers

Question # 10 of 10 (Start time: 12:08:54 PM, 09 August 2020)

How can you define an interrupt?

Select the correct option

O	A process where an external device can speedup the working of the microprocessor
O	A process where memory can speed up programs execution speed
◉	A process where an external device can get the attention of the microprocessor
O	A process where input devices can takeover the working of the microprocessor

2nd account

CS501:Semester Quiz # 01

ET = _____.

Select the correct option

○	CP x IC x T
◉	CPI x IC x T
○	CPI / IC x T
○	CPI x IC / T

Question # 2 of 10 (Start time: 12:20:59 PM, 09 August 2020)

In 8086/8088 processor, interrupt vector table is located at the memory location _____.

Select the correct option

⦿ 0

○ 4

○ 256

○ 1024

Question # 3 of 10 (Start time: 12:21:20 PM, 09 August 2020)

Every time you press a key, an interrupt is generated.
This is an example of

Select the correct option

⬛	Hardware interrupt
◯	Software interrupt
◯	All of the given
◯	None of the given

Question # 4 of 10 (Start time: 12:21:45 PM, 09 August 2020)

Most parallel I/O ports used with peripheral devices are mapped on a range of_____.

Select the correct option

○ Bus addresses

○ Direct memory Access

◉ contiguous addresses

○ Cache

Question # 5 of 10 (Start time: 12:22:05 PM, 09 August 2020)

Which is the last instruction of the ISR that is to be executed when the ISR terminates?

Select the correct option

| ●	IRET
○	IRQ
○	INT
○	NMI

Question # 6 of 10 (Start time: 12:22:22 PM, 09 August 2020)

How can you define an interrupt?

Select the correct option

○ A process where an external device can speedup the working of the microprocessor

○ A process where memory can speed up programs execution speed

◉ A process where an external device can get the attention of the microprocessor

○ A process where input devices can takeover the working of the microprocessor
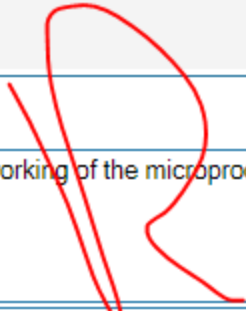
Question # 7 of 10 (Start time: 12:22:40 PM, 09 August 2020)

A software routine performed when an interrupt is received by the computer is called as ---------

Select the correct option

	Interrupt
○	
◉	Interrupt handler
○	Exception
○	Trap

Question # 8 of 10 (Start time: 12:23:00 PM, 09 August 2020)

Why DMA is faster than Programmer I/O technique because?

Select the correct option

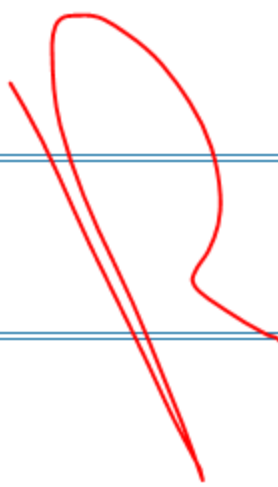|	
○	DMA transfers data directly using CPU.
◉	DMA transfers data directly without using CPU
○	DMA uses buffers with CPU
○	DMA uses interrupted driven I/O

Click to Save Answer &

Question # 9 of 10 (Start time: 12:23:16 PM, 09 August 2020)

Given an m-digit base b number x, the _____ of x is

$$x^c = (b^m - x) \bmod b^m$$
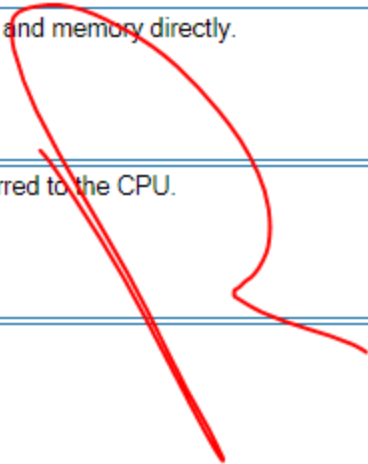
Select the correct option

⬤	Radix Compliment
◯	Diminished Radix Compliment
◯	Signed Magnitude Form
◯	Biased Representation

Question # 10 of 10 (Start time: 12:23:35 PM, 09 August 2020)

How does DMA saves CPU time?

Select the correct option

●	By controlling data transfer between I/O device and memory directly.
○	By storing all data in a buffer to be later transferred to the CPU.
○	By periodically polling.
○	By issuing an interrupt request to the CPU to request attention.

3rd account

Question # 1 of 10 (Start time: 12:26:20 PM, 09 August 2020)

Select the parts of a hard disk.

Select the correct option

○ Header only

○ Data section and a trailer

○ Data section only

◉ Header, data section and a trailer.

Question # 2 of 10 (Start time: 12:26:37 PM, 09 August 2020)

---------- allows a peripheral to read and write memory without intervention by the CPU.

Select the correct option

◯	Programmed I/O
◯	Interrupt driven I/O
◉	Direct memory access(DMA)
◯	Polling

Question # 3 of 10 (Start time: 12:26:54 PM, 09 August 2020)

The Pentium does allow the use of some part of its _____ accumulator register EAX

Select the correct option

○	8 bits
○	16 bits
●	32 bits
○	64 bits

Question # 4 of 10 (Start time: 12:27:07 PM, 09 August 2020)

For input ports, the incoming data should be placed on
the data bus only during the I/O read bus cycle. For this purpose, _____ are used.

Select the correct option

○	Flip Flops
◉	Tri-state Buffers
○	AND Gates
○	Registers

Question # 5 of 10 (Start time: 12:27:26 PM, 09 August 2020)   **Total Mark**

A component connected to the system bus and having control of it during a particular bus cycle is called_____.

Select the correct option

- ◯ Slave component
- ◉ Master component
- ◯ System bus
- ◯ Buffer component

Question # 6 of 10 (Start time: 12:27:44 PM, 09 August 2020)   **Total M**

Which I/O technique will be used by a sound card that may need to access data stored in the computer's RAM?

Select the correct option

○ Programmed I/O

○ Interrupt driven I/O

● Direct memory access(DMA)

○ Polling

Question # 7 of 10 (Start time: 12:28:01 PM, 09 August 2020)   Total Marks: 1

In the little-endian format exchanging data between computers, the data transmitted by one will be received in a "swapped" form by the other.

Select the correct option

○	Organized
○	Signals
◉	Swapped
○	Arranged

Question # 8 of 10 (Start time: 12:28:19 PM, 09 August 2020)   **Total Marks:**

A computer interface is an _____ circuit that matches the requirements of the two subsystems between which it is connected

Select the correct option

○ Digital

◉ Electronic

○ Primary

○ Obituary

Question # 9 of 10 (Start time: 12:28:35 PM, 09 August 2020)　　　　　　　　**Total Marks: 1**

In which one of the following methods for resolving the priority, the device with the highest priority is placed in the first position, followed by lower-priority devices up to the device with the lowest priority, which is placed last in the series?

Select the correct option

○　Asynchronous

◉　Daisy-Chaining Priority

○　Parallel

○　Semi-synchronous

Question # 10 of 10 (Start time: 12:28:52 PM, 09 August 2020)   **Total Marks:**

Every interrupt handler has an interrupt return (IRET) instruction, this instruction is an example of _____ return.

Select the correct option

○	NEAR
◉	FAR
○	SHORT
○	RELATIVE

4th account

Question # 2 of 10 (Start time: 12:36:11 PM, 09 August 2020)   **Tota**

A software routine performed when an interrupt is received by the computer is called as ---------

Select the correct option

	Interrupt
O	

	Interrupt handler
◉	

	Exception
O	

	Trap
O	
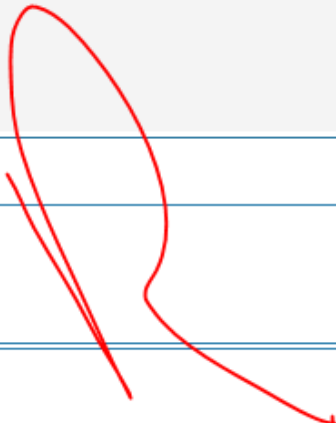
Identify the following type of serial communication error condition:
"The prior character that was received was not still read by the CPU and is over written by a new received character."

Select the correct option

○	Framing error
○	Parity error
◉	Overrun error
○	Under-run error

Question # 4 of 10 (Start time: 12:36:47 PM, 09 August 2020)

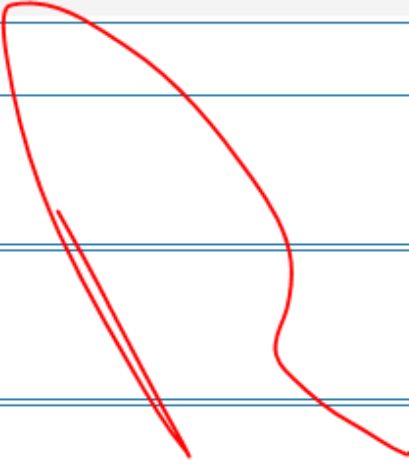Taking control of the system bus for a few bus cycles is known as _____.

Select the correct option

◯	Bus Stealing
◉	Cycle Stealing
◯	Cycle Transfering
◯	None of given

Question # 5 of 10 (Start time: 12:37:01 PM, 09 August 2020)

What is the status of the ACKNLG# signal when a character is completely received by the printer?
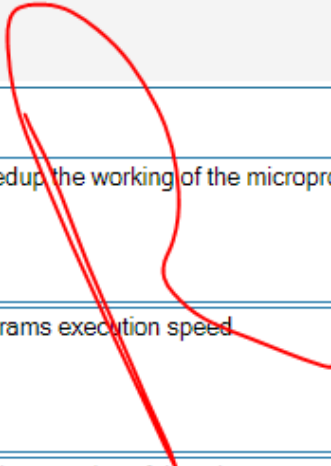
Select the correct option

○ It goes from low to high

◉ It goes from high to low

○ It toggles its state

○ It remains unaffected

How can you define an interrupt?

Select the correct option

○ A process where an external device can speedup the working of the microprocessor

○ A process where memory can speed up programs execution speed

◉ A process where an external device can get the attention of the microprocessor

○ A process where input devices can takeover the working of the microprocessor
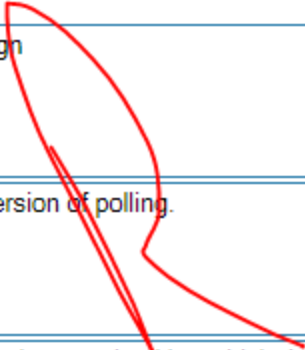
Question # 7 of 10 (Start time: 12:38:45 PM, 09 August 2020)

How Interrupt driven I/O is better than polling because?

Select the correct option

○	Interrupt driver I/O is easy to design
○	Interrupt driver I/O is enhanced version of polling.
◉	Interrupt driver I/O does not waste time on checking which device is available.
○	Interrupt driven I/O is easy to program.

For input ports, the incoming data should be placed on
the data bus only during the I/O read bus cycle. For this purpose, _____ are used.

Select the correct option

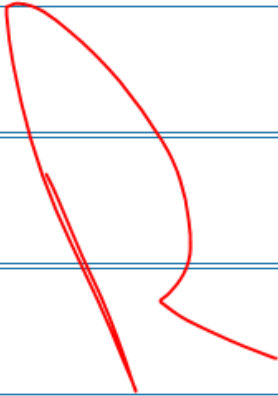|	
○	Flip Flops
◉	Tri-state Buffers
○	AND Gates
○	Registers

Question # 9 of 10 (Start time: 12:39:27 PM, 09 August 2020)   **Total Marks: 1**

In the little-endian format exchanging data between computers, the data transmitted by one will be received in a "swapped" form by the other.

Select the correct option

○	Organized
○	Signals
◉	Swapped
○	Arranged

Which I/O technique will be used by a sound card that may need to access data stored in the computer's RAM?

Select the correct option

○ Programmed I/O

○ Interrupt driven I/O

● Direct memory access(DMA)

○ Polling

5th account

Question # 1 of 10 (Start time: 12:53:10 PM, 09 August 2020)

Along with information bits we add up another bit which is called the_____bit.

Select the correct option

⚪	CRC
⚪	Hamming
⚪	Error Detection
⬛	Parity

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Tri-state buffers are used for removing _____.

Select the correct option

○	Instruction collision
○	bus collision
○	Instruction contention
◉	bus contention

Question # 3 of 10 (Start time: 12:53:49 PM, 09 August 2020)   **Total**

In which technique does the hardware directly access host memory for reading or writing independent of CPU?

Select the correct option

●	Direct Memory Access (DMA)
○	Programmed I/O
○	Interrupt driven I/O
○	Polling

Question # 4 of 10 (Start time: 12:54:12 PM, 09 August 2020)

The main issue/s in error control is/are_____.

Select the correct option

O	Detection of Error
O	Correction of Error
◉	Both Detection of Error and Correction of Error
O	Avoidance of Error

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Question # 5 of 10 (Start time: 12:54:26 PM, 09 August 2020)

The source file of FALSIM should contain _____ text only.

Select the correct option

○	Unicode
●	ASCII
○	ANSI
○	UTF

Question # 6 of 10 (Start time: 12:54:41 PM, 09 August 2020)

The directive _____ is used to define variables.
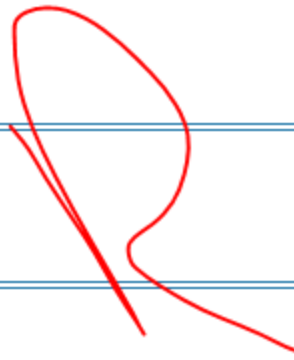
Select the correct option

	.equu
●	

	.db
○	

	.sw
○	

	.org
○	

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human works with base 10 and computers work with base _____.

Select the correct option

○	8
○	10
◉	2
○	16

The information about interrupt vector is given in 8-bits, from bit 0 to 7, which is translated to bit _____ on the data bus.
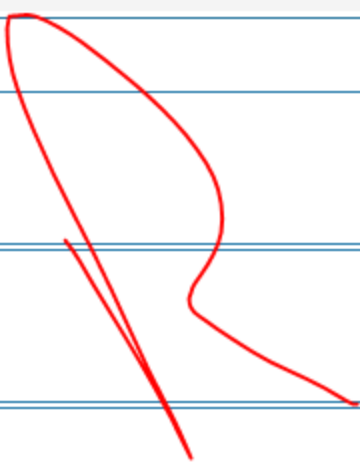
Select the correct option

◉ 16 to 23

○ 11 to 18

○ 0 to 7

○ 8 to 15

Question # 9 of 10 (Start time: 12:55:32 PM, 09 August 2020)

The _____ can be determined from the number of platters and the number of tracks.

Select the correct option

○	Speed of processing
○	execution time
◉	storage capacity
○	Latency

Question # 10 of 10 (Start time: 12:55:48 PM, 09 August 2020)

The conversion of numbers from a representation in one base to another is known as_____

Select the correct option

●	Radix Conversion
○	Number Representation
○	Decimal representation
○	Hexadecimal Representation