

بِسْمِ اللَّهِ الرَّحْمَنِ الرَّحِيمِ

In the Name of Allāh, the Most Gracious, the Most Merciful

Final-Term Papers Solved MCQS with Reference

1. DOS command _____ gives the status of the memory and also points out which memory area occupied by which process.

- **mem/d** PG # 13
- mem/e
- mem/m
- None of the Given

2. To store each character in keyboard buffer ____ bytes are required.

- **2** PG # 54
- 4
- 6
- 8

بری صحبت سے تنہائی بہتر ہے اور تنہائی سے نیک صحبت بہتر ہے

3.The interval timer is used to divide an input frequency.

- **True** PG #68
- False

4.The printer interface uses the _____.

- IRQ 6
- **IRQ 7** PG # 95
- IRQ 1
- None of given options

5.You can send _____ bits in parallel communication between two PC's at a time.

- 2
- 4
- 6
- **8**

6.In UART the line control register signify the _____.

- Word size and length of stop bits
- parity check and parity type
- Control bit
- **All of given** PG # 114

اللہ کا خوف سب سے بڑی دانائی ہے

7.The bit _____ of Line control register in UART, if set indicates that the DLL and DLM will act as the divisor register.

- 1
- 3
- 5
- **7**

PG # 114

8.Int 14H _____ can be used to set the line parameter of the UART or COM port.

- **Service # 0**
- Service # 1
- Service # 2
- Service # 3

PG #119

9. _____ is used to read time from RTC.

- **1A/02H**
- 1A/03H
- 1A/04H
- 1A/05H

PG # 137

10.The _____ signals are used by various devices to request a DMA operation.

- **DREQ**
- REQ
- DRE
- None of the given

PG #186

ایماندار کو غصہ دیر سے آتا ہے اور جلدی دور ہو جاتا ہے

11. DMA works in _____ modes.

- 2
- **3**
- 5
- 7

PG #188

DMA Modes Block Transfer, Single Transfer, Demand Transfer

12. DMA Request register can be used to simulate a DMA request through software in case of _____.

- I/O to memory transfer
- None of the given
- **Memory to memory transfer**
- Memory to I/O transfer

PG #192

13. _____ are the circular division of the disk.

- **Tracks**
- Sectors
- Blocks
- None of given

PG #201

Tracks are the circular division of the disk and the sectors are the longitudinal division of the disk

14. Blocks are _____.

- **Sectors per track**
- Track per sector
- Cylinder per track
- None of the given

PG #202

زندگی میں کامیابی کا یہی راز ہے کہ پریشانیوں سے پریشان مت بنو

15. Last ____ bytes of data part in partitioning table are the partition table signature.

- **2** **PG #218**
- 4
- 6
- 8

16. Information about further partitioned drives is available at _____ physical block of extended partition.

- **First** **PG #222**
- Second
- Third
- Fourth

17. _____ will be used to get drive parameters.

- Int 13H/42H
- **Int 13H/48H** **PG #238**
- Int 13H/66H
- Int 13H/66H

18. Inside a boot block operating system name occupies ____ bytes.

- 3
- 5
- **8** **Not sure**
- 11

دنیا کی سب سے بڑی فتح نفس پر قابو رکھنا ہے

19. A programmer wants to access the DPB (Drive Parameter Block) for C drive using undocumented service 21H/32H, what he should place in DL register _____ ?

- **0** PG #249
- 1
- 2
- 3

DL – 0 for current Drive

20. Blocks for FAT can be accessed using _____.

- BPB
- DPB
- **FCB**
- Both BPB and DPB.

21. In main memory smallest addressable unit is _____.

- **Byte**
- Nibble
- Word
- None of the given

22. Using the root directory entry and the _____, we can access the contents of File.

- Reserved Blocks
- Number of FAT copies
- **File Allocation Table (FAT)** PG # 241
- None of the given

23.If a file is having more than one cluster then it will be managed by _____.

- FAT
- BPB
- DPB
- **None of the given**

24.Practically in FAT 32, total number of entries are _____.

- 2^{26}
- 2^{28}
- 2^{30}
- **2^{32}**

25._____ keeps the back up of its boot block.

- FAT 12
- FAT 16
- **FAT 32**
- None of the given

26.NTFS can store file name in _____ form.

- **UNICODE** [Click Here For More Detail](#)
- Both ASCII and UNICODE
- None of the given
- ASCII

27.FAT based file system can store file name in _____ form.

- ASCII
- UNICODE
- **Both ASCII and UNICODE**
- None of the given

28.The LSN of first logical sector of NTFS partition is _____.

- **0** PG # 240
- 1
- 2
- 3

29.To minimize _____ defragmenter is used.

- **Seek time** PG # 316
- Access time
- Rotational delay
- Both seek and access time

30.In memory map of first 1 MB of RAM, the higher _____ is called system memory.

- 64 KB
- **384 KB** PG # 317
- 640 KB
- None of the given

Memory map of the first 1MB of RAM. The first 640KB is called conventional RAM and the higher 384KB is called system memory.

جھوٹ انسان اور ایمان دونوں کا دشمن ہے

31. Extended memory can be accessed in _____ mode.

- Real
- **Protected**
- Both real and protected
- None of the given

32. Extended memory can be accessed only after installing the driver _____ .

- EMM386.EXE
- **HIMEM.SYS** **PG # 318**
- IO.SYS
- None of the given

33. DOS uses _____ memory allocation system.

- **Contiguous** **PG # 325**
- Non Contiguous
- Both Contiguous and Non Contiguous
- None of the given

34. In protected mode segment register will be called _____.

- **Selector** **PG # 326**
- Descriptor
- IDT
- Both Selector and Descriptor

35. There are _____ Control registers in 80386 and above systems.

- 2
- **4**
- 8
- 16

PG # 331

36. If the CHS (cylinder, head, and sector) address of a disk is known it can be translated in to the _____ address and vice versa.

- Logical
- **LBA**
- Physical
- None of the Given

PG # 213

37. _____ is a data structure maintained by DOS in the boot block for each drive.

- DPB
- **BPB**
- FCB
- None of the Given

PG # 242

BIOS parameter block

” گفتگو ایسی چیز ہے۔
” جسکی وجہ سے انسان یا تو دل میں اتر جاتا ہے یا پھر دل سے اتر جاتا ہے۔“

38. System Stores the DPBs (Drive Parameter Block) for different drives in the form of _____.

- Chain / link list
- Array
- **Table** **Not sure**
- None of Given

39. The extension of a compiled TSR (Terminate and Stay Resident) program should be _____

- .EXE
- **.COM**
- .CPP
- .C

40. A single interrupt controller can arbitrate among _____ different devices.

- 4
- 6
- **8** **PG # 47**
- 10

41. The microprocessor package has many signals for data. Below are some in Correct priority order (Higher to Lower).

- Reset, Hold, NMI, INTR
- **NMI, INTR, Hold, Reset** **PG # 46**
- INTR, NMI, Reset, Hold
- None of the Given

42. A standard PC can have _____ PPI.

- 1
- **4**
- 8
- 16

PG # 84

A standard PC can have 4 PPI named LPT1, LPT2, LPT3 and LPT4

43. BIOS DO NOT support _____.

- LPT1
- LPT2
- LPT3
- **LPT4**

44. _____ is used to set date.

- 1A/02H
- 1A/03H
- 1A/04H
- **1A/05H**

PG # 139

45. If real time clock interrupts microprocessor then interrupt _____ will occur.

- 13h
- 17h
- **70h**
- 71h

46. _____ is LED control byte.

➤ 0xF3

➤ **0xED** PG # 181

➤ 0xE5

➤ 0xFF

47. The DMA requests to acquire buses through the _____ signal.

➤ **HOLD** PG # 186

➤ ACR

➤ ACK

➤ All of the given

48. _____ will specify if the next DMA transfer will happen as a single transfer, block transfer or demand transfer

➤ DMA Request register

➤ DMA Mask register

➤ **DMA Mode register**

➤ DMA Command register

49. _____ will specify if the channel is to cascade two DMA controllers.

➤ DMA Request register

➤ DMA Mask register

➤ **DMA Mode register**

➤ DMA Command register

50. _____ are the longitudinal division of the disk.

- Tracks
- **Sectors**
- Blocks
- None of given

PG # 201

Tracks are the circular division of the disk and the sectors are the longitudinal division of the disk

51. Highest physical capacity of the disk according to the IDE interface is _____.

- 504 MB
- 80 GB
- **127 GB**
- 300 GB

PG # 212

52. In partition table, first _____ bytes contain code which loads the boot block of active partition.

- 128
- 286
- **446**
- 500

PG # 218

53. On a single disk there can be _____ different Operating Systems.

- 2
- **4**
- 6
- 8

PG # 219

عقل مند کہتا ہے میں کچھ نہیں جانتا جبکہ بے وقوف کہتا ہے کہ میں سب کچھ جانتا ہوں

54. _____ is physical or absolute address.

- **LBA** **PG # 240**
- LSN
- CHS
- None of the given

55. The _____ block is the first block on disk.

- LSN =0
- **LBA=0** **PG # 240**
- Both LBA=0 and LSN=0
- None of the given

56. A directory is collection of _____.

- Bios Parameter Blocks
- Drive Parameter Blocks
- **File Control Blocks**
- None of the given

57. First cluster in user data area is numbered _____ in a FAT based systems.

- 0
- 1
- **2** **PG # 258**
- 3

58.Blocks for FAT can be accessed using _____.

- BPB
- DPB
- **FCB**
- Both BPB and DPB.

59.In FAT12 to calculate the address or offset from index, we need to multiply it with ____.

- 1/2
- **3/2** **PG 267**
- 5/2
- 7/2

60.Using the root directory entry and the _____, we can access the contents of File.

- Reserved Blocks
- Number of FAT copies
- **File Allocation Table(FAT)** **PG #269**
- None of the given

61.In FAT16 if FAT entry is between FFF0H to FFF6H then _____.

- Cluster is available
- **It is a Reserved cluster**
- It is next file cluster
- It is a last file cluster

62. _____ needed to store a UNICODE character.

- Nibble
- **2 Bytes**
- 4 Bytes
- Byte

63. _____ used for FCB in FAT 12 and FAT 16.

- Nibble
- Byte
- **2 Bytes**
- 4 Bytes

64. _____ file system is used in CD's.

- None of the given
- **Contiguous**
- Chained
- Indexed

65. FAT based file system can store file name in _____ form.

- ASCII
- UNICODE
- **Both ASCII and UNICODE**
- None of the given

66. In case of _____ contents, the indexes of clusters of file are stored in MFT.

- Small files
- Large files
- **Both small and large files**
- None of the given

67. In memory map of first 1 MB of RAM, the higher _____ is called system memory.

- 64 KB
- **384 KB**
- 640 KB
- None of the given

Memory map of the first 1MB of RAM. The first 640KB is called conventional RAM and the higher 384KB is called system memory

68. PC's operated on 20 bit address bus can operate in _____ mode.

- **Real** PG # 319
- Protected
- Both real and protected
- None of the given

خود کو تمہیں سے بڑھ کر کوئی اچھا مشورہ نہیں دے سکتا

69. Int 21H/52H returns the address of DOS internal data structures in ES: BX, the far address of the first MCB in memory lies _____ behind the address returned.

- 2-bytes
- **4-bytes**
- 6-bytes
- 8-bytes

70. In memory management, descriptor entry is of _____ bytes.

- **64**
- 32
- 8
- 16

71. There are _____ Control registers in 80386 and above systems.

- 2
- **4**
- 8
- 16

PG # 331

72. Control Registers are used for conveying certain control information for _____ Addressing and Co-Processors.

- Real Mode
- **Protected Mode**
- Virtual Mode
- None of the given

PG # 331

73.The least significant bit of Control register _____ is PE-bit which can be set to enable Protected Mode Addressing.

- **CR0**
- CR1
- CR2
- CR3

74.CD.. command gives the _____ of directory.

- None of the given
- One previous level
- One next level
- **Current path** PG #282

75.Cluster size is variable and can be measured in power of _____.

- **2**
- 16
- 4
- 8

کسی انسان کی خوبی کو پچھانوں اور اسے بیان کرو، لیکن اگر کسی کی
خامی مل جائے تو یہاں تمہاری خوبی کا امتحان ہے۔
فرمان حضرت علیؓ

76. Who notifies the end of interrupt (EOI) to the programmable interrupt controller?

- Programmer
- **Microprocessor**
- Operating system
- None of the given

77. Choose the correct statement to notify the interrupt controller about end of interrupt (EOI).

- Inportb(0x20,0x20)
- **Outportb(0x20,0x20)**
- Outportb(0x30,0x22)
- None of the given

78. Information about Pending interrupts are held by _____.

- Interrupt Service Register
- Interrupt Mask Register
- None of the Given
- **Interrupt Request Register**

79. BPB stands for _____.

- **BIOS parameter block**
- BIOS processing block
- Base processing block
- BIOS partition block

80.The input frequency used by the interval timer is the _____signal generated by the clock generator.

- None of Given
- **PCLK**
- ACL
- ICP

81.BIOS DO NOT support _____.

- LPT1
- LPT2
- LPT3
- **LPT4**

82.DTE is _____.

- **Data terminal equipment** PG #109
- Data transmitting equipment
- Dual terminal equipment
- None of the given.

83.Int 14H _____ can be used to send a byte.

- Service # 0
- **Service # 1**
- Service # 2
- Service # 3

جو شخص ناکامیوں سے ڈر کر بھاگتا ہے کامیابی اُس سے ڈر کر بھاگتی ہے

84. A single DMA can transfer _____ operands to and from memory in a single a bus cycle.

- **8 bit**
- 16 bit
- 32 bit
- 64 bbit

85. DMA does not work in _____ mode.

- **Double Mode**
- Block Transfer
- Demand Transfer
- None of the given

86. An addressable unit on disk can be addressed by three parameters _____.

- **head #, sector # and track #**
- HD #, AH# and track #
- Hd0 #, sector # and track #
- None of the given

87. Each addressable unit has a unique combination of sec#, head# and track# as its _____ address.

- Both Physical and Logical
- None of the given
- **Physical**
- Logical

جو لوگوں کے سامنے فخر کرتا ہے وہ لوگوں کی نظروں سے گر جاتا ہے

88. In partition table, first _____ bytes contain code which loads the boot block of active partition.

- 128
- 286
- **446**
- 500

89. On a single disk there can be _____ different Operating Systems.

- 2
- **4**
- 6
- 8

90. The last partition table within the chain contains _____ entry/entries about the logical drive.

- **Single**
- Double
- Three
- Four

91. Partition Table can be read using the extended _____ Services.

- 13H
- **14H**
- 15H
- 17H

92. File control block (FCB) is _____ byte long.

- 8
- 16
- **32**
- 64

93. _____ is collection of contiguous blocks.

- **Cluster**
- Sector
- File
- Directory

94. A FAT 12 table can have maximum _____ values.

- 1024
- 2048
- **4096**
- None of given

2^{12} entries maximum = 4096

95. The first cluster number of file can be found in _____.

- BPB
- DPB
- **FCB**
- None of the given

PG # 265

96. In FAT12 if FAT entry is 000H then _____.

- It is a last file cluster
- Cluster is available
- **It is a Reserved cluster**
- It is next file cluster

97. Total number of clusters of FAT12 are _____.

- FF0 H
- FFF H
- FEF H
- **FEE H** PG # 266

98. When we mark a file as deleted by placing 0xE5 then the chain of clusters in FAT is also replaced by _____.

- E5
- **0**
- 1
- N

99. To store a cluster in FAT 32 _____ is/are needed.

- Byte
- 2 Bytes
- **4 Bytes**
- Nibble

100. _____ keeps the back up of its boot block.

- FAT 12
- FAT 16
- **FAT 32**
- None of the given

101. Up to _____ bytes can be used to store a file name in NTFS.

- 30
- 126
- **254**
- 510

102. First logical sector of NTFS partition is _____.

- DPB
- MFT
- **Boot sector**
- None of the given

103. The LSN of first logical sector of NTFS partition is _____.

- **0**
- 1
- 2
- 3

104. In case of _____ the contents of file are stored in MFT.

- Small files
- Large files
- **Both small and large files**
- None of the given

105. Interrupt Descriptor Table can have up to _____ entries.

- 64
- 128
- **256**
- 512

106. There can be _____ different descriptors privilege levels.

- 2
- **4**
- 8
- 10

107. The least significant bit of Control register _____ is PE-bit which can be set to enable Protected Mode Addressing.

- **CR0**
- CR1
- CR2
- CR3

PG # 331

108.Entry point of execution in EXE File can be _____.

- From first instruction
- From last instruction
- **Anywhere in the program**
- From anywhere in the middle

PG # 335

109.In NTFS, Block # ____ is the safest block to store the back up of boot block.

- 8
- **10** **Not sure**
- 20
- 6

110.Size of single entry in Partition Table is _____.

- **512 bytes**
- 128 bytes
- 64 bytes
- 16 bytes

111.MBR partition table can have _____ entries at maximum.

- 1
- 2
- 3
- **4** **Not sure**

112. _____ is a data structure maintained by DOS in the boot block for each drive.

- DPB
- **BPB**
- FCB
- None of the Given

PG # 242

113. Hidden blocks between the first physical block on each partition are used by _____ for storing of data

- User
- Operating System
- BIOS
- **None of the Given**

114. Which of the following data structure is NOT used in Operating System's File management?

- **Memory Control Block**
- File Allocation Table
- Drive Parameter Block
- File Control Block

PG # 06

115. Interrupt procedures are _____ procedures.

- **Reentrant**
- Non-Reentrant
- Recursive
- Virtual

PG # 38

116. The extension of a compiled TSR (Terminate and Stay Resident) program should be _____

- .EXE
- **COM**
- .CPP
- .C

to make a .C program a TSR program, you are required to generate a .COM file by using the following command in DOS prompt.

```
bcc -EEXAMPLE.COM -IC:\BORLANDC\INCLUDE -LC:\BORLANDC\LIB EXAMPLE.C
```

Here, we are generating EXAMPLE.COM for of EXAMPLE.C program.

117. Choose the correct statement to notify the interrupt controller about end of interrupt (EOI).

- Inportb(0x20,0x20)
- **Outportb(0x20,0x20)**
- Outportb(0x30,0x22)
- None of the given

118. Information about Pending interrupts are held by _____.

- Interrupt Service Register
- Interrupt Mask Register
- **Interrupt Request Register**
- None of the Given

عقل مند اپنے عیب خود دیکھتا ہے اور بیوقوفوں کے عیب دنیا دیکھتی ہے

119. _____ Procedure can have parameters.

- Hardware Interrupt
- Software Interrupt
- **Both Hardware and software Interrupt** PG # 10
- None of the Given

120.The Address of partition block on hard disk is _____.

- head # =0, track # = 0 and sector # = 0
- head # =0, track # = 0 and sector # = 1
- head # =0, track # = 1 and sector # = 1
- **head # =1, track # = 0 and sector # = 1** PG # 42

121.NMI Stand for _____

- **Non Maskable Interrupt** PG #46
- Non Multitude Interrupt
- Non Maskable Instruction
- None of Given

122.The interval timer is used to divide an input frequency.

- **True** PG # 68
- False

بدصورت چہرہ بدصورت دماغ سے بہتر ہے

123.The PPI acts as an interface between the CPU and a parallel _____ .

- **I/O device** **PG # 86**
- CPU
- BUS
- None of Given

124.BIOS DO NOT support _____.

- LPT1
- LPT2
- LPT3
- **LPT4**

125._____ is used to identify the cause of interrupt.

- Interrupt Enable register
- **Interrupt ID register**
- Interrupt Status register
- None of the given

126.The bit _____ of Line control register in UART, if cleared will indicate that DLL is the data register.

- 1
- 3
- 5
- **7**

127.Int 14H _____ can be used to send a byte.

- Service # 0
- **Service # 1** **PG # 121**
- Service # 2
- Service # 3

Service of 14h include service #1 which is used to send a byte and service #2 which is used to receive a byte

128.To access battery powered RAM, only _____ ports are important from programming point of view.

- **70 and 71H** **PG # 141**
- 71 and 72H
- 70 and 72H
- 72 and 73H

129.The DMA requests to acquire buses through the _____ signal.

- **HOLD** **PG # 186**
- ACR
- ACK
- All of the given

130._____ used to program various common parameters of transfer for all the channels.

- None of the above
- DMA Status Register
- **DMA Command Register** **PG # 191**
- DMA Request Register

131.Bit # _____ of mode register in DMA determine the direction of a transfer.

- **2**
- 3
- 4
- 5

132._____ will specify if the next DMA transfer will happen as a single transfer, block transfer or demand transfer.

- DMA Request register
- DMA Mask register
- **DMA Mode register**
- DMA Command register

133.Each addressable unit has a unique combination of sec#, head# and track# as its _____ address.

- Both Physical and Logical
- None of the given
- **Physical** **PG # 202**
- Logical

134.Highest capacity of disk can be accessed using BIOS functions is _____.

- 128 MB
- 256 MB
- **504 MB** **PG # 211**
- 127 GB

عقل مند آدمی اس وقت تک نہیں بولتا جب تک خاموشی نہیں ہو جاتی

135.DOS has built in limit of _____ blocks per cluster.

- 32
- 64
- **128**
- 256

PG # 242

136.BIOS Parameter block is situated in _____ Block.

- **Boot**
- Data
- Extended Data
- None of Given

PG # 242

137.The _____ block is the first block on disk.

- LSN =0
- **LBA=0**
- Both LBA=0 and LSN=0
- None of the given

PG # 240

138.Inside a boot block jump code part occupies _____ byte.

- 5
- 8
- 11
- **3**

PG # 244

بہترین تجربہ وہ ہے جس سے نصیحت حاصل ہو

139. In boot block BIOS parameter block starts from _____.

- 03H
- 05H
- 08H
- **0BH**

140. In main memory smallest addressable unit is _____.

- **Byte**
- Nibble
- Word
- None of the given

141. Total number of clusters of FAT12 are _____.

- FF0 H
- FFF H
- FEF H
- **FEE H**

PG # 266

142. In FAT12 to calculate the address or offset from index, we need to multiply it with _____.

- 1/2
- **3/2**
- 7/2
- 5/2

PG# 267

143. Total number of cluster of FAT16 is _____.

- FFF0 H
- FFFF H
- FFEF H
- **FFEE H**

Page 272

144. Total ____ fragments can be supported for storing long file names.

- 26
- 28
- **32**
- 48

145. _____ used for FCB in FAT 12 and FAT 16.

- Nibble
- Byte
- **2 Bytes**
- 4 Bytes

146. To store a cluster in FAT 32 _____ is/are needed.

- Byte
- 2 Bytes
- **4 Bytes**
- Nibble

147. Practically in FAT 32, total number of entries are _____.

- 2^{26}
- 2^{28}
- 2^{30}
- **2^{32}**

148. First logical sector of NTFS partition is _____.

- DPB
- MFT
- **Boot sector**
- None of the given

149. Extended memory can be accessed in _____ mode.

- Real
- **Protected**
- Both real and protected
- None of the given

150. MCB is a _____ bytes data structure.

- 8
- **16**
- 32
- 64

151. Real mode does not support _____ memory allocation system.

- **Contiguous**
- Non Contiguous
- Both Contiguous and Non Contiguous
- None of the given

152. There can be _____ different descriptors privilege levels.

- 2
- **4**
- 8
- 10

153. In memory management, descriptor entry is of _____ bytes.

- 8
- 16
- 32
- **64**

154. For virus to propagate itself, it has to intercept interrupt _____.

- 9H
- 11H
- **13H**
- 17H

خوبصورتی علم و ادب سے ہوتی ہے لباس و حسن سے نہیں

155.If virus wants to be in memory independently, it should have its own _____.

- MCB
- PSP
- EB
- **Both MCB and PSP** **PG #334**

156.Size of single entry in Partition Table is _____.

- **512 bytes** **PG#218**
- 128 bytes
- 64 bytes
- 16 bytes

157._____ is a data structure maintained by DOS in the boot block for each drive.

- DPB
- **BPB** **PG#242**
- FCB
- None of the Given

158.Cluster size is variable and can be measured in power of _____.

- **2** **PG#242**
- 16
- 4
- 8

159.Bit # _____ of Eflag is used for alignment check

- 12
- 14
- 15
- **18**

PG # 164

160.Each addressable unit has a unique combination of sec#, head #, track # as its _____ address.

- **Physical**
- Logical
- Both
- None

PG #202

161.First cluster in user data is numbered in a FAT based system.

- 0
- 1
- **2**
- 3

PG # 258

162.BIOS services understand -----.

- **LBA**
- LSN
- Cluster #
- None

PG # 212

163.The first cluster number of a file can be found in-----

- BPB
- DPB
- **FCB**
- None

PG # 265

164.The size of FS Info block is

- 64byte
- 128 byte
- 256 byte
- **512 byte** **PG # 300**

165.In NTFS first ----- entries are reserved.

- 4
- 6
- **16** **PG # 303**
- 32

166.In memory map of first 1 MB of ram ,the first ----- is called conventional RAM.

- 64kb
- 384kb
- **640kb** **PG # 317**
- None

167.In memory map of first 1 MB of ram ,the higher ----- is called system memory.

- 64kb
- **384kb** **PG # 317**
- 640kb
- None

168.The ----- of boot block constitutes of BPB.

- Code part
- **Data part** **PG # 242**
- Both
- None

169. Extended BIOS function make use of ----- address

- **LBA** **PG # 212**
- CHS
- LSN
- None

170. LBA address can be used in place of the CHS address.

- **True** **PG # 235**
- False

171. In FAT12, the maximum range of clusters is

- 0 ~ FEFH
- 1 ~ FEFH
- **2 ~ FEFH** **PG # 266**
- 3 ~ FEFH

172. NTFS volume can be accessed directly in DOS.

- True
- **False** **PG # 310**

173. Each partition information chunk is 16 bytes long and the last two bytes at the end of the partition table data part is the partition table signature whose value should be _____ indicating that the code part contains valid executable code.

- 00AA
- 0055
- 050A
- **AA55** **PG # 219**

174. Service 21H/52H service returns the address of DOS internal data structures in ES: BX _____ behind the address returned lies the far address of the first MCB in memory.

- 2-bytes
- **4-bytes** PG # 322
- 6-bytes
- 8-bytes

175. 80386 can have _____ control registers.

- 2
- 5
- 3
- **4** PG # 331

176. The partition table uses the extended _____ service.

- **13H** PG # 234
- 14H
- 15H
- 16H

177. The entry point of execution in EXE File can be

- Start of the first instruction
- Start of the last instruction
- **Anywhere in the Program** PG # 335
- Can be in the middle of the program

انسان کے لئے بری صحبت سے بڑھ کر بری کوئی چیز نہیں

178.Using the____ entry and the FAT we can access the contents of file.

- Reserved blocks
- **Root Directory** PG # 269
- Number of FAT copies
- None of the given

179.Control information in files is maintained using

- BPB
- DPB
- **FCB** PG # 256
- FPB

180.What will happen if NTFS volume is accessed in DOS?

- Convert it to FAT volume
- Nothing will happen
- **Error of invalid media** PG # 310
- None of the given

181.LSN of FS Info block is available at

- BPB
- **FAT**
- Root Directory
- None of the given

خاموشی غصے کا بہترین علاج ہے

182.DOS device drivers do not understand the_____ data structures.

- FAT12
- FAT16
- FAT32
- **NTFS** **PG # 310**

183._____ is a collection of contiguous blocks.

- **Cluster** **PG # 242**
- Sector
- Byte
- None of Given

184._____ used to determine the amount of conventional memory interfaced with the processor in kilobytes.

- INT 10 H
- INT 11 H
- **INT 12 H** **PG # 162**
- INT 13 H

185.Bit number _____ of coprocessor control word is the Interrupt Enable Flag.

- **7** **PG #168**
- 8
- 9
- 10

186.To distinguish 486 with Pentium CPUID Test is used.

- **True** **PG # 166**
- False

187. Practically _____ entries are there in FAT 32.

- 2^{26}
- 2^{28}
- 2^{30}
- **2^{32}** PG # 265

188. BPB stands for _____.

- **BIOS parameter block** PG # 243
- BIOS processing block
- Base processing block
- BIOS partition block

189. The keyboard input character scan code is received at ___ port.

- **60H** PG # 179
- 61H
- 62H
- 63H

190. _____ is LED control byte.

- 0xFD
- **0xED** PG # 181
- 0xFF
- 0xEE

جھوٹ رزق کو کہا جاتا ہے

191. _____ means typematic rate will be sent in next byte.

- **0xF3** PG # 180
- 0xF4
- 0xF5
- 0xF6

192. Keyboard uses port _____ as status port.

- **64H** PG # 177
- 66H
- 67H
- 69H

193. The keyboard can perform _____ serial I/O.

- asynchronous
- **synchronous**
- Multiple
- Single

194. Bit number 2 of port 64H Status register used for output buffer full.

- True
- **False**

195. Bit number _____ can declare the parity error of port 64H Status register.

- 4
- 5
- 6
- **7**

196.Bit number _____ of port 64H Status register used for input buffer full.

- **0**
- 1
- 2
- 3

197.Disadvantage of FAT32 is _____.

- Large disk size can be managed in FAT32
- Cluster size is reduced
- Internal fragmentation is reduced
- **Very large table PG # 299**

198. Maximum possible entries in FAT12 are _____.

- 1024
- 2048
- **4096 PG # 264**
- 65536

199. What will be the value of the word located at 1Fh in DPB when number of free clusters on drive is not known?

- 0000H
- 1111H
- **FFFFH PG # 250**
- None of the given.

افضل انسان وہ ہے جو اپنی اصلاح کی کوشش کرتا ہے

200. Jump code part contains ____ bytes in boot block.

- **3** **PG # 302**
- 5
- 8
- 11

201. Operating system name contains ____ bytes in boot block.

- 3
- 5
- **8** **PG # 257**
- 11

202. File can be _____ viewed as organization of data.

- Physically
- **Logically** **PG # 256**
- Both logically and physically
- None of the give

203. _____ is used to read a block against its LSN.

- **absread()** **PG # 247**
- abswrite()
- lsread()
- None of the given

204. File can be_____ viewed as collection of clusters or blocks.

- **Physically** PG # 256
- Logically
- Both physically and logically
- None

205. When we talk about FAT based file system, in user data area first cluster number is _____.

- 0
- 1
- **2** PG # 258
- None of the given

206. Cluster number can also be referred as block number.

- True
- **False** PG # 258

207.To access the block within cluster using BIOS services the cluster number should be converted into _____.

- CHS
- LBA
- **LSN** PG # 258
- None of the given

208.What will be the value of DL register when we are accessing C drive using undocumented service 21H/32H?

- 0
- 1
- 2
- **3** PG # 249

209.The directory structure of DOS is like _____.

- Array
- **Tree** PG # 256
- Linked list
- None of the given

210.Control information about files is maintained using _____.

- BPB
- DPB
- **FCB** PG # 256
- FPB

211.When LSN is equal to zero (0), it means _____.

- First block of the disk
- **First block of the logical drive** PG # 240
- First block of hidden blocks
- None of the given

212.In FAT32, lower _____ bits are used.

- 26
- **28** PG # 292
- 30
- 32

213._____ is relative address with respect to the start of Logical Drive.

- LBA
- **LSN** PG #240
- CHS
- None of the given

214.The practical limit of blocks per cluster is _____.

- 32 blocks per cluster
- **64 blocks per cluster** PG #242
- 128 blocks per cluster
- 256 blocks per cluster

215.In dos we have limit of _____.

- **128 blocks per cluster** PG #242
- 256 blocks per cluster
- 32 blocks per cluster
- 64 blocks per cluster

216.Highest capacity physical capacity of the disk according to the IDE interface is _____.

- **127 GB** PG # 212
- 100 GB
- 80 GB
- 300 GB

217.Partition Table can be read using the extended _____ Services.

- **13 H** PG # 234
- 14 H
- 15 H
- None of given

218.In Protected Mode, the segment registers are used as _____

- Descriptor
- **Selector** PG # 326
- All of the given choices
- None of the given choices

219.To access drive parameter block we use undocumented service _____

- 09H/32H
- 11H/32H
- 17H/32H
- **21H/32H** PG # 249

220. _____ is an absolute address relative to the start of physical drive.

- **LBA** PG # 240
- LSN
- CHS
- None of the above

221.Boot block consists of _____ bytes.

- 64
- 128
- 256
- **512** PG # 242

222.The DMA requests to acquire buses through the _____ signal.

- **HOLD** PG #186
- ACR
- ACK
- None of Given

اس سے پہلے کہ تمہیں شہوت فتنے میں ڈالے نکاح کرلو

223. The keyboard device writes a code 0xFA on the port 60H to indicate that the _____.

- Input buffer is full
- **Byte has been received properly** PG # 179
- Output buffer is full
- None of the given

224. A single DMA can transfer _____ operands to and from memory in a single bus cycle.

- **8-bits** PG # 186
- 16-bits
- 32-bits
- 12-bits

225. In FAT12, to calculate the address or offset from index, we need to multiply it with ____.

- 1/2
- **3/2** PG #267
- 5/7
- 7/2

226. _____ Register can be used to show that the channel is single transfer, block transfer or demand transfer mode.

- DMA Command register
- DMA Request Register
- DMA Mode Register
- **DMA controller Register** PG #187-188

227. When we mark a file as deleted by placing 0xE5 then the chain of clusters in FAT is also replaced by _____.

- E5
- 1
- **0** PG # 79
- N

228. Cluster size is reduced in _____.

- FAT12
- FAT16
- **FAT32** [Click here for detail](#)
- None of the given

229. In FAT32 _____ root directory entries are there.

- 128
- 256
- 512
- **None of the given** [Click here for detail](#)

230. If a file is having more than one cluster then it will be managed by _____.

- FAT
- BPB
- DPB
- **None of the above**

بری صحبت سے تہائی بہتر ہے اور تہائی سے نیک صحبت بہتر ہے

231. Internal fragmentation is reduced in _____.

- FAT12
- **FAT16** [Click here for detail](#)
- FAT32
- None of the given

232. For supporting long file names, _____ fragments can be supported.

- 12
- 20
- 26
- **32**

233. To store a cluster in FAT 32 _____ is/are needed.

- Nibble
- Byte
- 2 Bytes
- **4 Bytes** [Click here for detail](#)

234. If a file size is 12K and the size of the cluster is 4K then _____ clusters are used for the file.

- 2
- **3**
- 4
- 5

ایماندار کو غصہ دیر سے آتا ہے اور جلدی دور ہو جاتا ہے

235. We can access the contents of File by using the root directory entry and _____.

- Reserved Blocks
- Number of FAT copies
- **File Allocation Table (FAT)** PG # 269
- None of the given

236. FAT based file system can store file name in _____ form.

- ASCII
- UNICODE
- **Both ASCII and UNICODE**
- None of the given

237. Drive parameter block is derived from _____.

- FCB
- FAT
- **BPB** PG # 249
- CPB

238. We can access Blocks for FAT using _____.

- BPB
- DPB
- **FCB**
- Both BPB and DPB

جھوٹ انسان اور ایمان دونوں کا دشمن ہے

239.If we know the cluster number, we can access the blocks within the cluster using BIOS services directly.

- **True** PG # 258
- False

240._____ is an internal data structure of DOS and resides in main memory.

- BPB
- DPB
- CPB
- **None of the given.** [Click here for detail](#)

241.The size of DPB data structure is _____ bytes.

- 16
- 32
- 64
- **128** [click here for detail](#)

242.The size of FCB data structure is _____ bytes.

- **16** [Click here for detail](#)
- 32
- 64
- 128

243.Advantages of FAT32 is/are _____.

- Large disk size can be managed in FAT32
- **Cluster size is reduced** [Click here for detail](#)
- Internal fragmentation is reduced
- All of the given

ایماندار کو غصہ دیر سے آتا ہے اور جلدی دور ہو جاتا ہے

244. _____ file system keeps the backup of its boot block.

- FAT12
- FAT16
- **FAT32** [Click here for detail](#)
- None of the given

245. To store a UNICODE character _____ is/are needed.

- Nibble
- Byte
- **2 Bytes** [Click here for detail](#)
- 4 Bytes

246. _____ is the first block on disk.

- LSN =0
- **LBA=0** PG # 240
- LBA=1
- Both LBA=0 and LSN=0

247. If FAT entry is between FFF0H to FFF6H in FAT16 then _____.

- Cluster is available
- **It is a Reserved cluster** PG # 272
- It is next file cluster
- It is a last file cluster

خود کو تمہیں سے بڑھ کر کوئی اچھا مشورہ نہیں دے سکتا

248. File system used in CD's is _____ file system

- **Contiguous** [Click here for detail](#)
- Chained
- Indexed
- None

249. A file has 2 clusters and the size of cluster is 4K. What will be the size of file?

- 2K
- **8K**
- 16K
- 32K

250. In NTFS, Backup of boot block is stored at block # _____.

- 2
- 6
- **8**
- 10

251. The interval timer can operate in _____ modes.

- Five
- Seven
- Four
- **Six** PG # 72

252. File control block (FCB) is _____ byte long.

- **32** [Click here for detail](#)
- 64
- 16
- 128

253.DOS command _____ which gives the status of the memory and also points out which memory area occupied by which process.

- **mem/d** PG # 13
- mem/e
- mem/m
- None of the given

254.Each entry in the IVT is _____ in size.

- **4-bytes** PG # 12
- 6-bytes
- 8-bytes
- 2-bytes

255.The tail value of the keyboard buffer should be examined to get to the _____ of the buffer.

- **Start** PG # 56
- End
- Middle
- None of given

256.Usually interrupt procedures are reentrant procedures especially those interrupt procedure compiled using C language compiler are reentrant.

- **True** PG # 38
- False

جو شخص ناکامیوں سے ڈر کر بھاگتا ہے کامیابی اُس سے ڈر کر بھاگتی ہے

257. _____ is Disk interrupt.

- 10H
- 11H
- **13H PG # 42**
- 14H

258. In parallel communication, the maximum numbers of bits we can send between two computers are _____.

- 2-bits
- 4-bits
- 6-bits
- **8-bits**

259. 14h include _____ which is used to send a byte.

- Service #0
- **Service #1 PG # 121**
- Service #2
- Service #3

260. The status register _____ is the main control register.

- **B PG # 146**
- A
- C
- D

261. _____ is used to identify the cause of interrupt.

- **Interrupt ID Register PG # 116**
- PC Register
- AC Register
- None of All These

262. In NTFS, up to _____ characters are used to store file names,

- 30
- 48
- **255 PG # 283**
- 510

263. A cluster is a collection of contiguous _____.

- **Blocks PG # 242**
- Sectors
- Bytes
- None of Given

264. In BPB, root directory is saved in _____.

- **Cluster#0**
- Cluster#1
- Cluster#2
- Cluster#3

265. In NTFS, total sizes of MFT entries are _____.

- **16-bytes PG # 303**
- 20-bytes
- 26-bytes
- 32-bytes

266. In NTFS, _____ store the contents of file.

- **Both small & large file Record**
- Small record
- Large Record
- None of given

267. In NTFS, contents and indexed of file is stored in _____.

- Small record
- Large Record
- Both small & large file Record
- **None of given**

268. Total No. of bytes that can be stored in Keyboard Buffer is _____.

- 16
- **32 PG #54**
- 64
- 128

269. BIOS support _____ UARTS as COM ports.

- 6
- **4 PG #113**
- 3
- 2

270. DCE stands for _____.

- **Data communication equipment PG # 109**
- Distributed Computing Environment
- Data Communications Equipment
- Data Carrier Equipment

271. In counter register bit no. 3 changes its value between 0 and 1 with in _____ clock cycles

- 1
- 2
- 4
- **16 PG #69**

272. In _____ each byte is needed to be encapsulated in start and end.

- Synchronous communication
- **Asynchronous communication** PG # 106
- Both
- None of given

273. The _____ service # is not used in any interrupt.

- 01
- 02
- 03
- **FF**

274. If we want to send printing on the printer then we have to perform following steps.

- Initialize printer
- Read Status
- Check Error
- **All of the given**

275. If printer is _____ then printer sends back the ACK signal to the printer interface

- **idle** PG # 97
- busy
- Out of paper
- None of the given

276. DSR stands for _____.

- **Data set ready** PG # 111
- Data service ready
- Data stock ready
- None of the given

277. At IRQ 7 Interrupt # ____ is used.

- 0x0A
- 0x0B
- 0x0C
- **0x0F** PG # 95

278. The memory addresses of COM ports remain same for all computers

- True
- **False**

279. In keyboard status byte bit no. 2 and 3 are used for ctrl and alt keys respectively. which of the following condition is used to check that Ctrl + Alt keys are pressed. Where: unsigned char far * scr = (unsigned char far *) (0x00400017);

- **if (((*scr)&12)==12)**
- if (((*scr)&8)==8)
- if (((*scr)&4)==4)
- if (((*scr)&2)==2)

280. In case of synchronous communication a timing signal is required to identify the start and end of a bit.

- **True** PG # 105
- False

281. The baud rate is set in accordance with the divisor value loaded within the UART internal registers base +0 and base +1.

- **True** PG # 114
- False

عقل مند آدمی اس وقت تک نہیں بولتا جب تک خاموشی نہیں ہو جاتی

282. Software based flow control make use of ----- control characters

- Xon
- XOFF
- **Both** PG # 135
- None

283. _____ is used to read time from RTC

- **1A\02H** PG # 137
- 1A\03H
- 1A\04H
- 1A\05H

284. Int _____ service 0 can be used to set the line parameter of the UART or COM port.

- **14H** PG # 119
- 15H
- 13H
- None of the given option

285. When LBA is equal to zero (0), it means _____.

- **First block of the disk** PG # 240
- First block of the logical drive
- First block of the hidden block
- None of the given

286. In IRQ2 and IRQ3 which one has the highest priority?

- Can't be determined
- Both have same priority
- IRQ3
- **IRQ2** PG # 47

287. Following is not a method of I/O

- Programmed I/O
- Interrupt driven I/O
- **Hardware Based I/O** PG # 4
- None of given

288. It is possible to perform I/O operations from three different methods.

- **True** PG # 7
- False

289. The Function of I/O controller is to provide _____.

- I/O control signals
- Buffering
- Error Correction and Detection
- **All of given** PG # 5

290. Which of the following are types of ISR _____.

- BIOS (Basic I/O service) ISR
- DOS ISR
- ISR provided by third party device drivers
- **All of the given** PG # 13

291. Interrupt service number is usually placed in _____ register.

- CH
- CL
- **AH** PG # 26
- AL

خوبصورتی علم و ادب سے ہوتی ہے لباس و حسن سے نہیں

292.NMI Stand for

- **Non Maskable Interrupt** PG # 46
- Non Multitude Interrupt
- Non Maskable Instruction
- None of Given

293.A single interrupt controller can arbitrate among ____ different devices.

- 4
- 6
- **8** PG # 47
- 10

294. The microprocessor package has many signals for data. Below are some incorrect priority order (Higher to Lower).

- **Reset, Hold, NMI, INTR** PG # 46
- NMI, INTR, Hold, Reset
- INTR, NMI, Reset, Hold
- None of the Given

295.The following command “outportb (0x61,inportb(0x61) & 0xFC);” will

- Turn on the speaker
- **Turn off the speaker** PG # 75
- Toggle the speaker
- None of the given

296.The following command “outportb (0x61,inportb(0x61) | 3);” will _____ .

- **Turn on the speaker** PG # 74
- Turn off the speaker
- Toggle the speaker
- None of the above

297.The PPI acts as an interface between the CPU and a parallel _____ .

- **I/O device** **PG # 83**
- CPU
- BUS
- None of Given

298.BIOS DO NOT support _____.

- LPT1
- LPT2
- LPT3
- **LPT4** **PG # 91**

299._____ bit is cleared to indicate the low nibble is being sent.

- D1
- D2
- D3
- **D4** **PG # 104**

300.The bit _____ of Line control register in UART, if cleared will indicate that DLL is the data register.

- 1
- 3
- 5
- **7** **PG # 114**

301._____ file system is used in NTFS based systems.

- Contiguous Chained
- **Indexed**
- None of the given

302. Communication between keyboard and keyboard controller is _____.

- Asynchronous serial
- **Synchronous serial**
- Parallel communication
- None of the given

PG # 77

303. In NTFS, boot sector is stored at

- First and 6th sector
- First and Last sector
- Only at Last sector
- **Only at First sector**

304. Standard PC operates in two modes in terms of memory which are

- Real mode and Extended Mode
- Base mode and Memory Mode
- None of the given

- **Real mode and protected mode** PG # 6

305. IVT is a table containing _____ byte entries each of which is a far address of an interrupt service routine.

- 2
- **4**
- 8
- 16

PG # 20

306. Each paragraph in keep function is _____ bytes in size.

- 4
- 8
- **16**
- 32

PG # 24

307.A software interrupt does not require EOI (End of interrupt).

- **True** **PG # 49**
- False

308.To store each character in keyboard buffer ____ bytes are required.

- **2** **PG # 54**
- 4
- 6
- 8

309.Interrupt ____ is empty; we can use its vector as a flag.

- 9H
- 13H
- 15H
- **65H** **PG # 65**

310.Command register is an ____ bit register

- 4
- **8** **PG # 71**
- 16
- 32

311.How many bytes can be used to store a file name in NTFS?

- 128
- **255**
- 510
- 1024

ہر چیز کی ایک پہچان ہوتی ہے اور عقلمند کی پہچان غور و فکر کرنا ہے اور غور و فکر کی پہچان خاموشی ہے

312. ____ is the first logical sector of NTFS partition.

- DPB
- MFT
- **Boot sector**
- None

313. In boot block BIOS parameter block starts from 03H

- 05H
- 08H
- **0BH** **PG # 302**

314. In NTFS, FAT and root directory is replaced by

- FCB
- **MFT** **PG # 301**
- Hidden blocks
- Boot sector

315. Block # 2 is the safest block to store the backup of boot block.

- True
- **False**

316. The keyboard interface as discussed earlier uses the IRQ0 and the port 64H as data port.

- True
- **False**

317. FAT12 will have 12-bit wide entries and can have $2^{12}=4096$ entries maximum

- **True**
- False

318. In order to produce the sound from PC internal Speaker we have to load the ___ bit divisor value at the ___ port.

- 8, 0x21
- **16, 0x42**
- 32, 0x22
- 64, 0x32

319. DMA stands for _____

- **Direct Memory Access** **PG # 4**
- Distinct Memory Access
- Direct Module Access
- Direct Memory Allocation

320. UART stands for _____

- **Universal Asynchronous Receiver Transmitter** **PG # 107**
- Universal Adjustment and Realigning Tool
- Unconventional Assisted Recovery Team
- None of these

321. Interrupt Vector Table (IVT) in short is a _____ bytes sized table.

- **1024** **PG # 10**
- 2048
- 3072
- 4096

322. Hardware Interrupts are _____.

- Preemptive
- **Non-Preemptive** **PG # 48**
- Both Preemptive and Non-Preemptive
- None of Given

329. _____ is used to control the printer via the BIOS

- Int 16H
- **Int 17H**
- Int 18H
- Int 19H

PG # 84

330. There are two main types of interrupts namely _____.

- PC based and Window based
- Hardware based and Kernal based
- **Hardware interrupts and Software interrupts** PG # 10
- None of the given

331. To set the interrupt vector means is to change the double word sized interrupt vector within the IVT.

- **True** PG # 22
- False

332. The service number is usually placed in the _____ register.

- AL
- CL
- **AH** PG # 26
- AX

333. The keyboard makes use of interrupt number _____ for its input operations.

- **9** PG # 34
- 10
- 11
- 12

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334.The service _____ is called the keyboard hook service.

- 15H/2FH
- **15H/4FH** PG # 44
- 15H/FFH

335.The BIOS interrupt _____ can be used to configure RTC.

- **1AH** PG # 136
- 2AH
- 3AH
- 4AH

336.PPI stands for

- Parallel Programmable interface
- **Peripheral Programmable interface** PG # 76
- Port Programmable interface
- None of the given

337.Int _____ is used to control the printer via the BIOS.

- **17H** PG # 84
- 18H
- 20H
- 21H

338.Counter register can be used to divide clock signal.

- **True** PG # 69
- False

دنیا میں سب سے مشکل کام اپنی اصلاح اور سب سے آسان کام دوسروں پر نکتہ چینی کرنا ہے

339. The bit number _____ of the coprocessor control word is the interrupt enable flag.

- **7** **PG # 168**
- 8
- 9
- 6

340. There are _____ kinds of serial communication.

- **2** **PG # 105**
- 3
- 4
- 5

341. _____ store the base address for LPT1.

- 40:00H
- 40:02H
- **40:08H** **PG # 92**
- 40:1AH

342. The amount of memory above conventional memory (extended memory) can be determined using the service _____.

- **15H/88H** **PG # 162**
- 16H/88H
- 17H/88H
- 21H/88H

343. The output on the monitor is controlled by a controller called _____ within the PC.

- **Video controller** **PG # 30**
- Bus controller
- Ram controller
- None of the given

344. Interrupt 9 usually reads the _____ from keyboard.

- ASCII code
- **Scan code** PG # 34
- Both ASCII and Scan code
- None of the above

345. NMI Stand for

- **Non Maskable Interrupt** PG # 46
- Non Multitude Interrupt
- Non Maskable Instruction
- None of Given

346. A single interrupt controller can arbitrate among _____ different devices.

- 4
- 6
- **8** PG # 47
- 10

347. The microprocessor package has many signals for data. Below are some in Correct priority order (Higher to Lower).

- **Reset, Hold, NMI, INTR** PG # 46
- NMI, INTR, Hold, Reset
- INTR, NMI, Reset, Hold
- None of the Given

348. The _____ function initialize the COM port whose number is passed as parameter using BIOS services.

- Initializecom()
- **Initialize()** PG # 125
- Recievechar()
- None of these option

349. There are two types of communication: synchronous and Anti Synchronous

- True
- **False** **PG # 105**

350. REGS is a Union

- **True**
- False

351. Keyboard Status Byte is located at the address

- 0040:0000H
- 0040:0013H
- 0040:0015H
- **0040:0017H** **PG # 29**

352. If we use `keep (0, 1000)` in a TSR program, the memory allocated to it is

- 64000 bytes
- 32000 bytes
- **16000 bytes**
- 80000 bytes

353. Maximum number of interrupts in a standard PC is

- 64
- 128
- **256** **PG #10**
- 512

354. The ----- function receives a byte and COM port number is passed as parameter using BIOS service

- `Receivebyte ()`;
- `Receive ()`;
- **`Receivechar ()`**; **PG # 125**
- None of the given options

355. _____ whenever receive indicates the start of communication whenever receive indicates the end of communication

- **XON\XOFF** **PG #135**
- XOFF\XON
- XON\YOFF
- YON\XOFF

356. _____ is used to set time from RTC

- 1A\02H
- **1A\03H** **PG #138**
- 1A\04H
- 1A\05H

357. Set the Interrupt vector means to change the double word sized interrupt vector within IVT.

- **True** **PG #22**
- False

358. If keyboard buffer is empty the head and tail points at the same location.

- **True** **PG #55**
- False

359. Standard PC can have _____ PPI.

- 1
- **4** **PG #84**
- 8
- 16

360. By cascading two DMAs _____ bits can be transferred.

- 4
- 8
- **16** **PG #186**
- 32

361.PPI interconnection _____ bits is cleared to indicate low nibble is being sent.

- D1
- D2
- D3
- **D4** **PG # 101**

362.Display device (Monitor) performs _____ I/O.

- **memory mapped** **PG #30**
- Isolated
- Both of above
- None of these

363.Timer interrupt occurs _____ times every second by means of hardware.

- **18.2** **PG # 28**
- 16.2
- 15.2
- 14.2

364.An I/O device cannot be directly connected to the busses so controller is placed between CPU and I/O.

- **True** **PG # 83**
- False

365.Tail of keyboard should get to get the start of buffer.

- **True** **PG # 55**
- False

366.____ No. of bytes are used to store the character in the keyboard buffer.

- 1
- **2** **PG # 54**
- 4
- 8

367. We have set the bit No. 7 of IMR (Interrupt Mask Register) to unmask the Interrupt so that interrupt _____ can occur at _____ line.

- **0xf, IRQ 7**
- 0xa, IRQ 6
- 0x8, IRQ 5
- 0x6, IRQ 2

368. If we want to produce the grave voice from speaker phone then we have to load the _____ divisor values at Port _____.

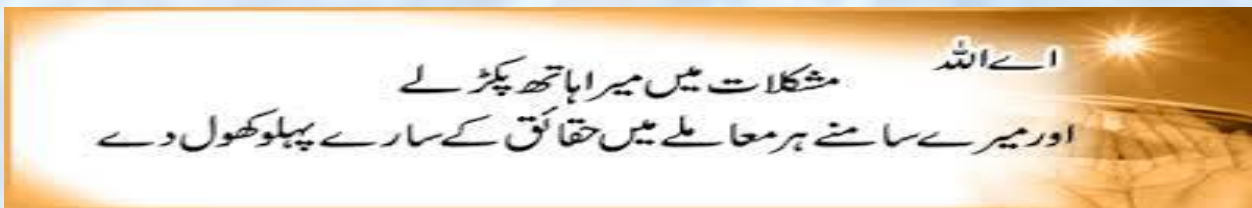
- high, 0x42
- low, 0x22
- high, 0x22
- **low, 0x42**

369. On the execution of IRET instruction, number of bytes popped from stack is

- 4 bytes
- 6 bytes
- **8 bytes** [Click here for detail](#)
- 10 bytes

370. If CPUID instruction is not present then the processor can be a

- **486 processor** PG # 166
- 386 processor
- 286 processor
- All of the above



371. Extended memory is available if the processor is of the type _____

- **AT** **PG # 171**
- XT
- All of the given choices
- None of them

372. The built in mechanism within the UART for error detection is _____

- hamming code
- **parity** **PG # 107**
- CRC16 (cyclic redundancy check 16 bit)
- CRC32 (cyclic redundancy check 32 bit)

373. If three Programmable interrupt controllers are cascaded then how many interrupt driven hardware IO devices can be attached _____

- 12
- 18
- 23
- **24** **PG #48**

374. Int 14H _____ can be used to send a byte

- Service#0
- **Service#1** **PG #121**
- Service#2
- None of the given option.

375. Int 14H _____ can be used to set the line parameter of the UART or COM port.

- **Service # 0** **PG # 119**
- Service # 1
- Service # 2
- None of the given options

376.Int 14H _____ can be used to receive a byte.

- Service # 0
- Service # 1
- **Service # 2** **PG # 121**
- None of the given options

377.The _____ function simply enables the self test facility within the modem control register

- STOn()
- SelfTest()
- **SelfTestOn()** **PG # 127**
- None of these

378._____ is a device incorporated into the PC to update time even if the computer is off.

- Clock counter
- ROM
- Clock
- **Real time clock** **PG # 136**

379.Interrupt _____ is used to get or set the time.

- 0AH
- **1AH** **PG #136**
- 2AH
- 3AH

380._____ is used to set time.

- 1A/02H
- **1A/03H** **PG # 138**
- 1A/04H
- 1A/05H

381.----- is used to read date from RTC

- 1A\02H
- 1A\03H
- **1A\04H** **PG # 138**
- 1A\05H

382.____ whenever received indicates the start of communication and _____ whenever received indicates a temporary pause in the communication.

- **XON & XOFF** **PG # 135**
- XOFF & XON
- XON & YOFF
- YON & XOFF

383.The_____ function uses the COM port number to receive a byte from the COM port using BIOS services.

- recievebyte()
- initialize ()
- receive()
- **recievechar()** **PG # 125**

384.In self test mode the output of the UART is routed to its input.

- **True** **PG # 117**
- False

385.Only _____ ports are important from programming point of view.

- **70 and 71H** **PG # 141**
- 71 and 72H
- 70 and 72H
- 72 and 73H

386.The BIOS interrupt 0x1AH can be used to configure real time clock

- **True** **PG # 136**
- False

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*Winning is not everything,
but wanting to win is
everything.....
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